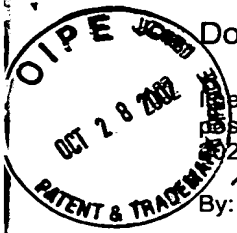


#6
2817-14
11-04-02



Docket No.: P2000,0150

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

By: [Signature]

Date: October 23, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Michael Asam
Applic. No. : 09/919,229
Filed : July 31, 2001
Title : Mixer Circuit Configuration
Art Unit : 2817

RECEIVED
OCT 30 2002
TECHNOLOGY CENTER 2800

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,570,056 (Groe), dated October 29, 1996;

United States Patent No. 5,933,771 (Tiller et al.), dated August 3, 1999;

F. Svelto et al.: "A Low-Voltage Topology For CMOS RF Mixers", *IEEE Transactions on Consumer Electronics*, Vol. 45, no. 2, May 1999, pp. 299-309;

European Search Report dated September 26, 2002.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicant

*Mark P. Weichselbaum
Reg. No. 43,248*

Date: October 23, 2002

Lerner And Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/bmb



FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.:
P2000,0150Applic. No.
09/919,229

Applicant

Michael Asam

Filing Date
July 31, 2001Group Art Unit
2817

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,570,056	10/29/96	Groe			
	B	5,933,771	08/03/99	Tiller et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	F. Svelto et al.: "A Low-Voltage Topology For CMOS RF Mixers", <i>IEEE Transactions on Consumer Electronics</i> , Vol. 45, no. 2, May 1999, pp. 299-309
	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.